

4DM4 Lab #1 SUBMISSION FORMAT:

Since some of the class did not receive these instructions by email, Lab #1 is now due Tuesday at 12:00 noon.

Please create a folder called 4DM4-Lab-1-XX-YY, where XX and YY are the initials of your team members.

In the folder, put your lab-1 report (a PDF or word file), and your well-documented VHDL code. (Your code can also be placed in the word or PDF document, that is fine.) The report should summarize Part A and Part B. For part A, please show the plots of your experiments, and have a short verbal summary. For part B, please take a few screen snap-shots to show that your switch works, and a short verbal summary. Please include your documented VHDL code. Please mention in your report which day you demonstrated the switch to our TA.

You do not need to put a lot of effort into preparing this report, if you have demonstrated that everything works to the TA. Please summarize what you have done concisely, so we each have a record of what you have done.

Please do not change the name of the folder after you compress it, or it will expand to a different name and we will never find it. When the folder name is correct, then compress the folder and email the compressed file to the prof, with this subject:

4DM4-Lab-1-SUBMISSION-XX-YY, where XX and YY are the initials of your team members.

Please CC your lab partner. If your lab partner does not receive the email, then the prof probably did not receive the email.