

PRACTICAL OP-AMPS.

Ideal op amp model



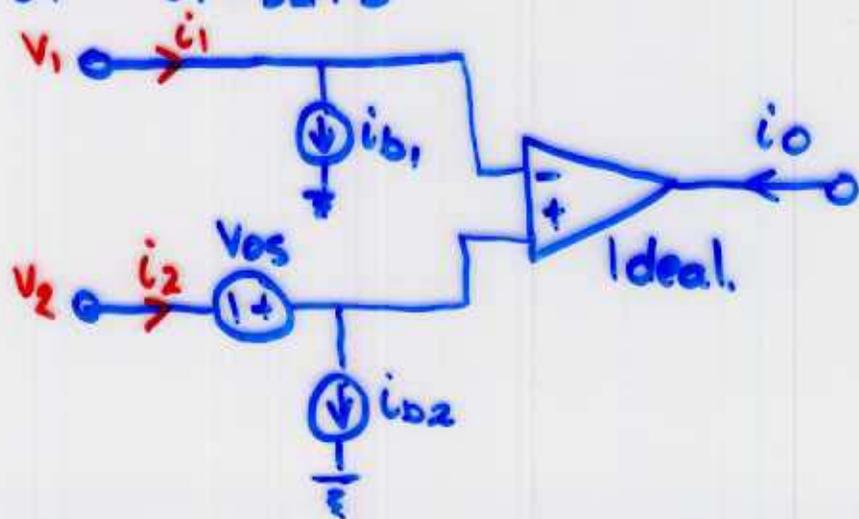
$$i_1 = i_2 = 0$$
$$V_1 = V_2$$

Practical op amps

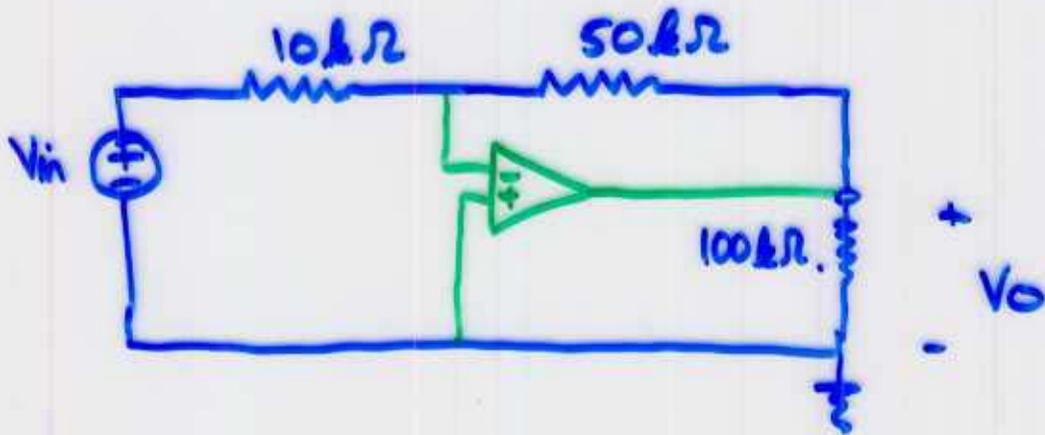
* Dominant imperfections

- non zero bias currents
- non zero input offset voltage
- finite input resistance
- non zero output resistance
- finite voltage gain

→ MODEL OF OFFSETS

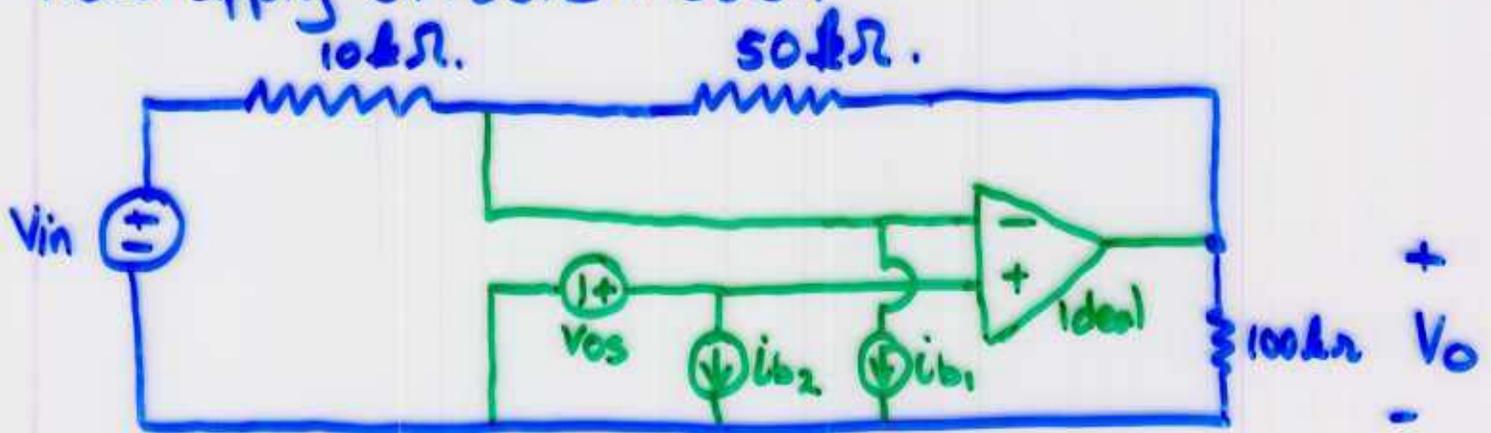


APPLICATION OF THE OFFSETS MODEL



Ideal Op-Amp
 $\Rightarrow \frac{V_o}{V_{in}} = -5$

Now apply offsets model.

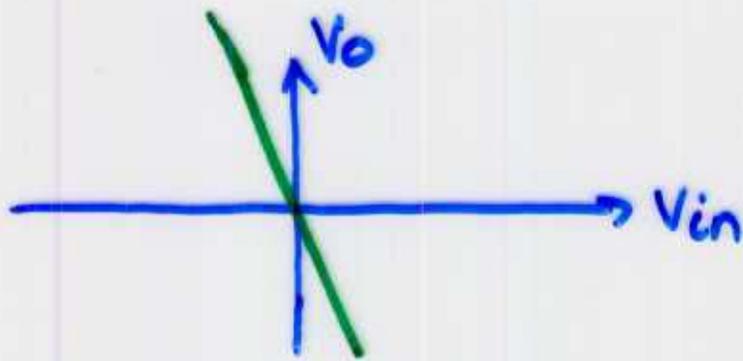


Now find V_o .

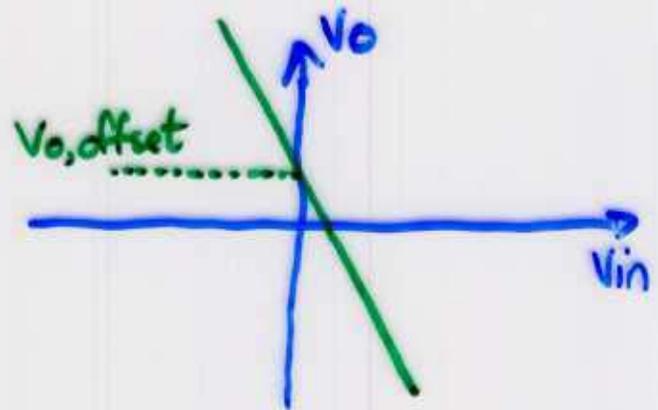
Using Superposition,

$$V_o = -5 V_{in} + 6 V_{os} + 50 \times 10^3 i_{b1}$$

$$= -5 V_{in} + V_{o,offset}$$



ideal case



offset case.

For this circuit

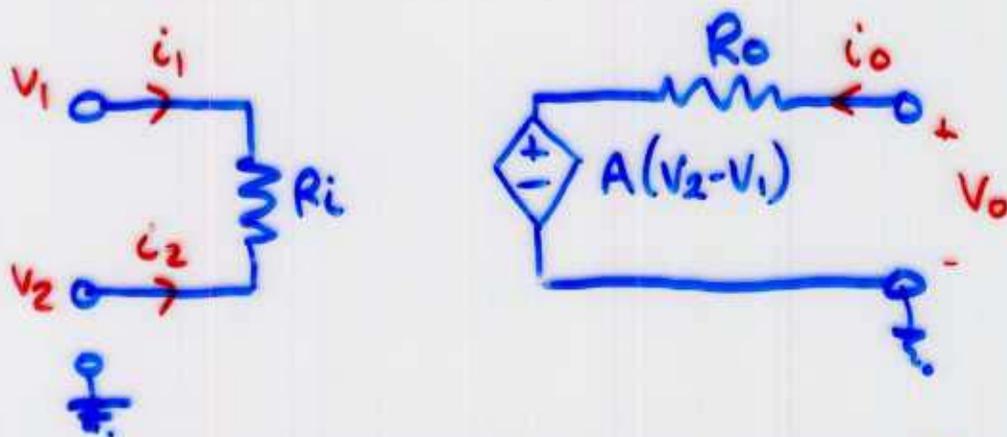
$$V_{o,offset} = 6 V_{os} + 50 \times 10^3 I_{b1}$$

For $\mu A471$ chip $|V_{os}| \leq 5 \text{ mV}$, $|I_{b1}| \leq 500 \text{ nA}$.

$$\Rightarrow |V_{o,offset}| \leq 55 \text{ mV}$$

\Rightarrow we can only ignore it if $V_{in} \gg 10 \text{ mV}$

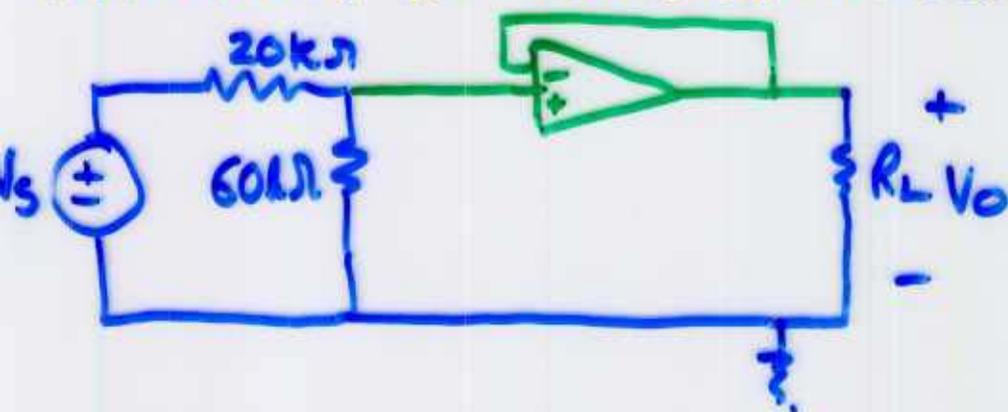
FINITE GAIN MODEL



By Replacing the ideal op amp in the offsets model by the finite gain model, we get a combined model.

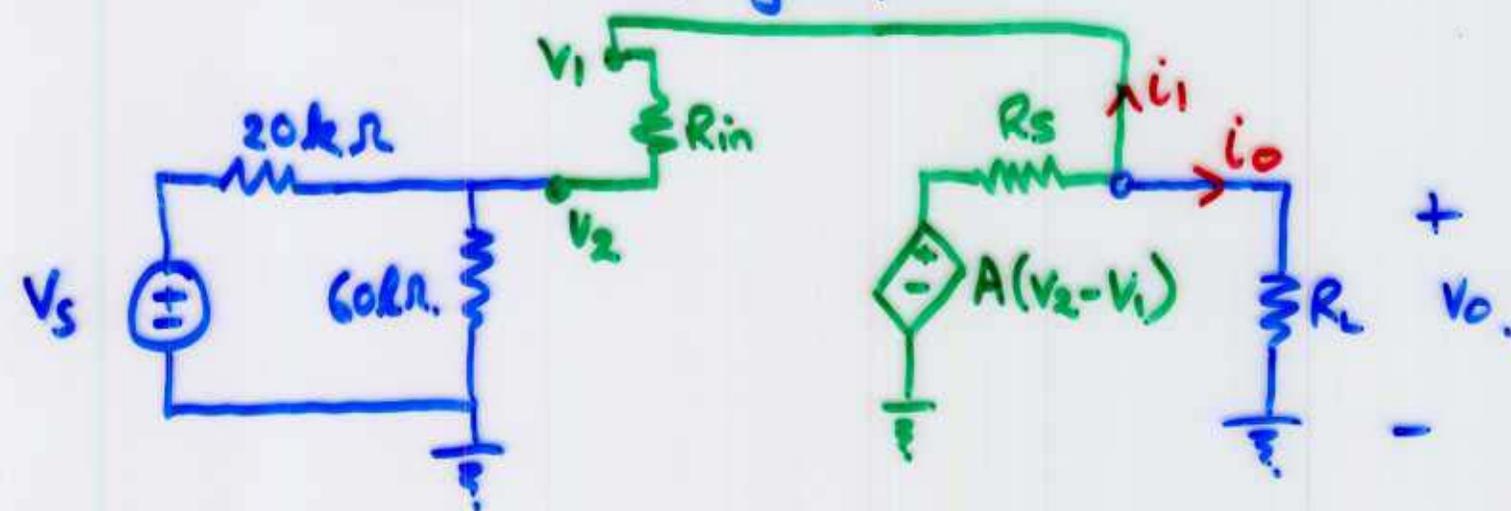
Typical values	($\mu A741$)	[Magnitude bounds]
Bias current, i_{b1}, i_{b2}	80 nA	$ i_{b1} - i_{b2} < 20 \text{ nA}$
offset voltage, V_{OS}	1 mV	
Input resistance, R_i	2 M Ω	
Output resistance, R_o	75 Ω	
Differential gain, A	200×10^3 V/V	

APPLICATION OF FINITE GAIN MODEL TO A BUFFER.



if op-amp is linear + ideal, $\frac{V_o}{V_s} = \frac{3}{4}$

what happens if it is linear, has very small offsets, but a finite gain.



Procedure is Node analysis (straight forward)

Algebra is harder than we have seen before.

KCL at output node



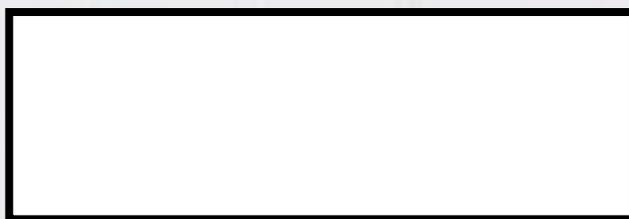
①

Voltage across R_{in}

$$i_1 = \frac{V_1 - V_2}{R_{in}}$$

②

KCL at node 2



③

Short circuit feedback

$$V_1 = V_0$$

④

\Rightarrow 4 equations, 4 unknowns, V_1, V_2, V_0, i_1

$$\textcircled{1}, \textcircled{2}, \textcircled{4} \Rightarrow V_2 \left(\frac{A}{R_S} + \frac{1}{R_{in}} \right) = V_0 \left(\frac{1}{R_{in}} + \frac{A+1}{R_S} + \frac{1}{R_L} \right)$$

$$\Rightarrow V_2 = V_0 \left(\frac{R_L R_S + (A+1) R_{in} R_L + R_{in} R_S}{\cancel{R_{in} R_S} R_L} \right) \left(\frac{\cancel{R_S} \cancel{R_{in}}}{A R_{in} + R_S} \right)$$

⑤

$$\textcircled{2}, \textcircled{3} \Rightarrow V_s \left(\frac{1}{20k} + \frac{1}{R_{in}} \right) = V_2 \left(\frac{1}{60k} + \frac{1}{20k} + \frac{1}{R_{in}} \right) - \frac{V_o}{R_{in}}$$

Use $\textcircled{5}$

$$V_s \left(\frac{R_{in} + 20k}{20k \cdot R_{in}} \right) = V_o \left\{ \left[\frac{R_L R_s + (A+1) R_{in} R_L + R_{in} R_s}{R_L (A R_{in} + R_s)} \right] \left[\frac{4 R_{in} + 60k}{60k R_{in}} \right] - \frac{1}{R_{in}} \right\}$$

$$\Rightarrow V_o = V_s \left(\frac{1 + 20k/R_{in}}{20k} \right) \times \left(\frac{60k R_L (A R_{in} + R_s) R_{in}}{(R_L R_s + (A+1) R_L R_{in} + R_s R_{in}) (4 R_{in} + 60k) \dots \dots \dots - 60k R_L (A R_{in} + R_s)} \right)$$

Now Rewrite.

$$V_o = V_s \left(\frac{1 + 20k/R_{in}}{20k} \right)$$

$$\times \left(\frac{60k R_L \left(1 + \frac{R_s}{A R_{in}} \right)}{4 R_L \left(1 + \frac{R_s}{A R_{in}} + \frac{1}{A R_{in}} + \frac{R_s}{A R_L} \right) \left(1 + \frac{60k}{4 R_{in}} \right) - R_L \left(\frac{60k}{R_{in}} + \frac{R_s}{A R_{in}} \right)} \right)$$

For the $\mu A471$ chip

$$A = 2 \times 10^5, \quad R_{in} = 2 \times 10^6 \quad R_s = 75 \Omega \approx 10^2$$

$$\Rightarrow \frac{R_s}{A R_{in}} \sim 10^{-9}, \quad \frac{1}{A R_{in}} \sim 10^{-11}$$

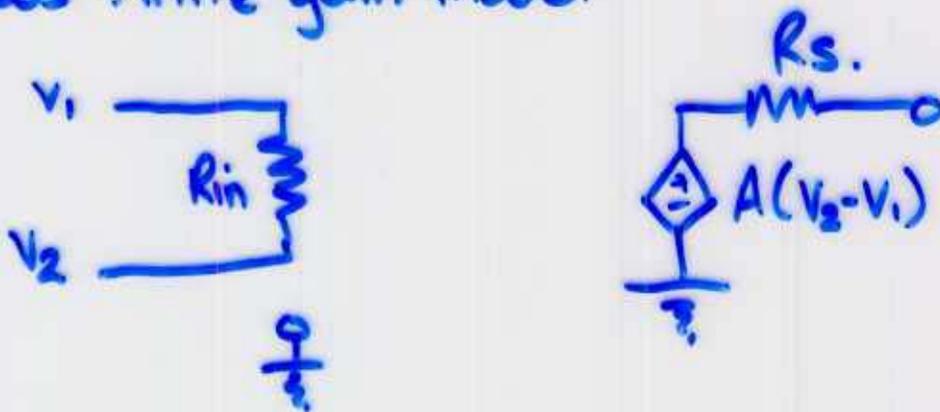
$$\frac{R_s}{A R_L} \sim \frac{10^{-3}}{R_L}, \quad \frac{60k}{4 R_{in}} \sim 10^{-2}$$

if all these terms become negligible, then.

$$\begin{aligned} V_o &\approx V_s \frac{1}{20k} \frac{60k R_L}{4 R_L} \\ &= \frac{3 V_s}{4} \end{aligned}$$

COMMON MODE REJECTION RATIO.

Previous finite gain model



A better finite gain model has a dependent source of the form.

$$A(v_2 - v_1) + A_{cm} \left(\frac{v_2 + v_1}{2} \right)$$

$v_2 - v_1$	called	differential voltage.
A	"	differential gain
$(v_2 + v_1)/2$	"	common mode voltage
A_{cm}	"	common mode gain

$$CMRR = \frac{A}{A_{cm}}$$

Typical value 30×10^3

Bandwidth.

we have only considered DC circuits

For AC circuits the gain A depends on frequency.

ie. if $v_2 - v_1 = M \sin \omega t$

then dependent source is

$$A(\omega) M \sin \omega t$$

This means that you have to pick the right op amp for your frequency range.