

TRANSIENT ANALYSIS

SO FAR WE HAVE CONSIDERED THE OPERATION OF TRANSMISSION LINES UNDER STEADY-STATE CONDITIONS WITH AC INPUT. IN THIS SECTION, WE CONSIDER THE TRANSIENTS IN A TRANSMISSION LINE EXCITED WITH DC INPUT. LET US FIRST ANALYZE THE CASE IN WHICH THE LINE IS TERMINATED BY A MATCHED LOAD, $R_L = Z_0$. NOTE THAT FOR A ~~lossless~~ LOSSLESS LINE, $R=0$ & $G=0$ & HENCE,

$$Z_0 = \sqrt{\frac{R+jWL}{G+jWC}} = \sqrt{\frac{L}{C}} \text{ IS REAL.}$$

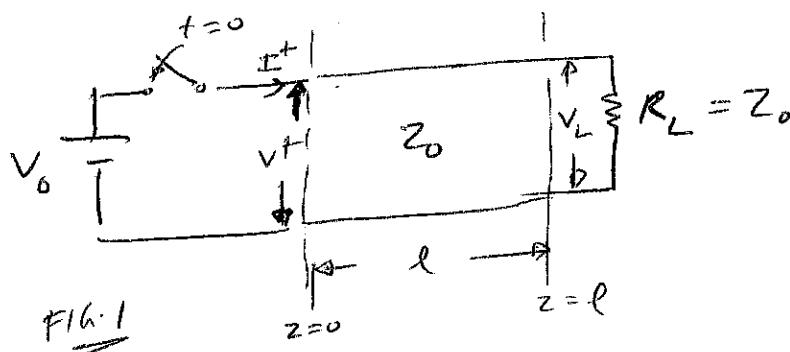


FIG. 1

At $t=0$, THE SWITCH IS CLOSED & THE LINE VOLTAGE AT THE INPUT (~~at $t=0$~~), ~~IS~~ V_t^+ BECOMES EQUAL TO THE BATTERY VOLTAGE, V_0 . At $t=0$, THE LOAD VOLTAGE V_L IS ZERO BECAUSE IT TAKES A CERTAIN TIME FOR THE VOLTAGE WAVE TO APPEAR ~~at~~ AT THE LOAD.

(a) $z = 0$

(2)

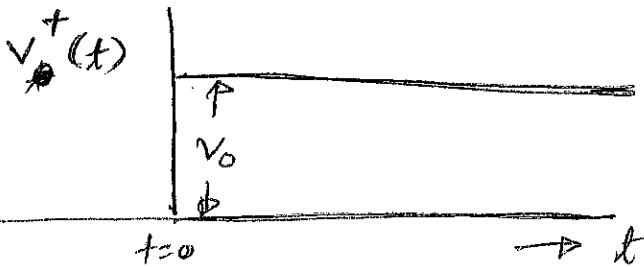


Fig.2

AS CAN BE SEEN THE LINE VOLTAGE AT THE INPUT IS A STEP FUNCTION. THE VOLTAGE WAVE PROPAGATES AT SPEED v . THE VOLTAGE AT ~~A~~ z , IN THE LINE IS SHOWN BELOW.

(a) $z = z_1$

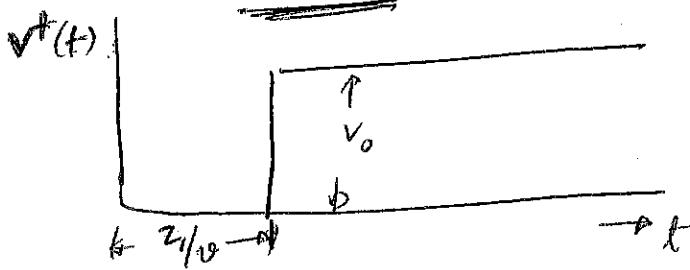


Fig.3

THE TIME TAKEN BY THE VOLTAGE WAVE TO TRAVERSE THE LINE IS l/v & HENCE LOAD VOLTAGE ~~TRANSMISSION~~ OR ~~TIME~~ IS DELAYED BY l/v AS SHOWN BELOW

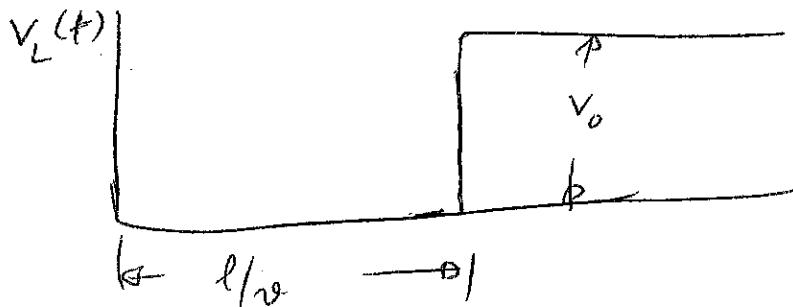


Fig.4

(3)

THE CURRENT IN THE LINE IS

$$I^+ = \frac{V^+}{Z_0}$$

THE TIME-DEPENDENCE OF I^+ IS THE SAME AS THAT OF V^+ SHOWN IN FIGS. 2 & 3 (WITH V_0 REPLACED BY ~~V_0~~)
 V_0/Z_0 IN FIGS. 2 & 3). THE LOAD CURRENT AT $t = l/v$
 IS $I_L = \cancel{V_0/Z_0} = V_0/R_L$. TO SUMMARIZE,

$$V_L = V_0 \quad \text{FOR } t > l/v \rightarrow (1)$$

$$= 0 \quad \text{OTHERWISE}$$

$$I_L = V_0/R_L \quad \text{FOR } t > l/v \rightarrow (2)$$

$$= 0 \quad \text{OTHERWISE}$$

NEXT, LET US CONSIDER A MORE GENERAL CASE IN WHICH THE
 LOAD IS NOT MATCHED TO THE LINE ($R_L \neq Z_0$). LET US ALSO
 INTRODUCE A SOURCE RESISTANCE R_s .

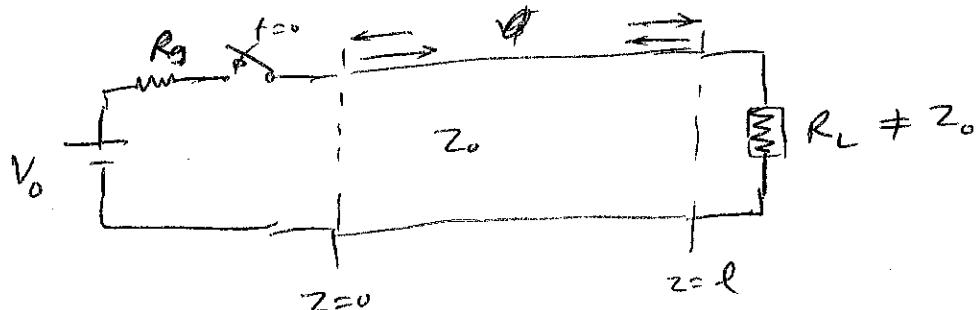


Fig. 5

(4)

At $t = 0$, the switch is closed & a voltage wave v_1^+ propagates to the right. After reaching the load, the voltage wave will reflect, producing a back-propagating wave v_1^- .

$$\frac{v_1^-}{v_1^+} = \Gamma_L = \frac{R_L - Z_0}{R_L + Z_0} \rightarrow (2)$$

HERE, Γ_L IS THE REFLECTION COEFFICIENT AT THE LOAD.
 (NOTE: THE SUBSCRIPT L IS ADDED BECAUSE WE HAVE REFLECTIONS
 AT THE SOURCE, TOO).

THE VOLTAGE IN THE LINE AFTER THE (FIRST) REFLECTION
 IS $v_1^+ + v_1^-$. AN ~~AT THE SOURCE~~ THE REFLECTED

VOLTAGE v_1^- HEADS TOWARDS THE SOURCE. AT THE
 SOURCE, LET US SUPPOSE $R_s \neq Z_0$. BECAUSE OF THE
 IMPEDANCE MISMATCH, THE v_1^- WAVE IS REFLECTED
 AT THE SOURCE TO PRODUCE A NEW FORWARD WAVE

 v_2^+

$$\frac{\text{REFLECTED } v_2^+}{\text{INCIDENT } v_1^-} = \Gamma_g = \frac{R_g - Z_0}{R_g + Z_0} \rightarrow (4)$$

INCIDENT

(∴ "AS SEEN" BY v_1^- , THE LOAD IS R_g)

(5)

THE NEW FORWARD WAVE V_2^+ PROPAGATES TOWARDS THE LOAD. AT THIS TIME, THE VOLTAGE IN THE LINE IS

$$V_1^+ + V_1^- + V_2^+ = V_1^+ + \Gamma_L V_1^+ + \Gamma_g V_1^-$$

$$= V_1^+ + \Gamma_L V_1^+ + \Gamma_L \Gamma_g V_1^+$$

→ (5)

V_2^+ IS REFLECTED AT THE LOAD TO PRODUCE A NEW BACKWARD WAVE V_2^- .

$$\frac{V_2^-}{V_2^+} = \Gamma_L$$

AFTER MANY ROUND TRIPS, THE ^{LINE} LOAD VOLTAGE IS
(i.e. STEAD STATE)

$$V_L = V_1^+ + V_1^- + V_2^+ + V_2^- + V_3^+ + V_3^- + \dots$$

$$= V_1^+ \left(1 + \underbrace{\Gamma_L}_x + \underbrace{\Gamma_L \Gamma_g}_x + \underbrace{\Gamma_L^2 \Gamma_g}_x + \underbrace{\Gamma_L^3 \Gamma_g^2}_x + \dots \right)$$

$$= V_1^+ \left(1 + \underbrace{\Gamma_g \Gamma_L}_x + \underbrace{\Gamma_g^2 \Gamma_L^2}_x + \dots \right) + \Gamma_L \left(1 + \underbrace{\Gamma_L \Gamma_g}_x + \underbrace{\Gamma_g^2 \Gamma_L^2}_x + \dots \right)$$

$$= V_1^+ (1 + \Gamma_L) (1 + \Gamma_g \Gamma_L + \underbrace{\Gamma_g^2 \Gamma_L^2}_{+\dots}) \rightarrow (6)$$

$$= \frac{V_1^+ (1 + \Gamma_L)}{(1 - \Gamma_g \Gamma_L)} \rightarrow (7)$$

(6)

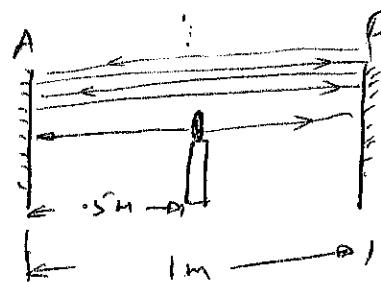
\therefore FOR A GEOMETRIC SERIES, $1 + a + a^2 + \dots = \frac{1}{1-a}$ if $|a| < 1$

$$= \frac{1}{1-a} \text{ if } |a| < 1$$

$$|\Gamma_s| < 1 \text{ & } |\Gamma_l| < 1$$

~~IT CAN BE SHOWN THAT $\Gamma_s \Gamma_l < 1$~~)

CONSIDER THE FOLLOWING ANALOGY: AN OBJECT IS PLACED BETWEEN THE MIRRORS THAT ARE 1 m APART.

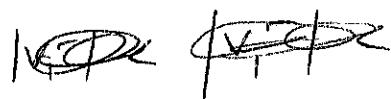


AS THE LIGHT FROM THE OBJECT FALLS ON THE LEFT MIRROR, ONE COULD SEE A VIRTUAL IMAGE OF THE OBJECT (APPEARS TO BE AT DISTANCE 1.5 m BEHIND THE MIRROR), WHICH I CALL THE FIRST VIRTUAL IMAGE. THE LIGHT REFLECTED FROM MIRROR A IS ALSO REFLECTED FROM MIRROR B & PROPAGATES TOWARDS THE MIRROR A, PRODUCING A ^{SECOND} VIRTUAL IMAGE (APPEARS TO BE AT DISTANCE 2.5 m BEHIND THE MIRROR A). THE BRIGHTNESS OF THE SECOND IMAGE IS LESS THAN THAT OF THE FIRST IMAGE SINCE THE REFLECTION COEFFICIENT Γ_A & Γ_B OF THE MIRRORS IS ALWAYS LESS THAN UNITY. THIS PROCESS CONTINUES & THERE ARE

(7)

INFINITE NUMBER OF VIRTUAL IMAGES IN BOTH MIRRORS UNDER THE STEADY-STATE CONDITIONS. HOWEVER, WE MAY BE ABLE TO SEE THREE OR FOUR IMAGES DEPENDING ON THE QUALITY OF THE MIRRORS. SIMILARLY,

IN THE CASE TRANSMISSION LINE,



$$\dots |V_2^-| < |V_2^+| < |V_1^-| < |V_1^+| \quad \& \text{ HENCE THE}$$

SUM IN EQ. (6) CONVERGES.

AS AN EXAMPLE, LET $R_g = 0$;

$$R_g = \frac{R_g - Z_0}{R_g + Z_0} = -1$$

AT $A=0$, IN THIS CASE, VOLTAGE AT THE INPUT OF THE LINE, $V_1^+ = V_o$. FROM (7), THE STEADY-STATE LOAD

VOLTAGE IS

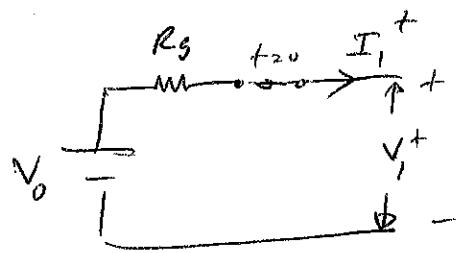
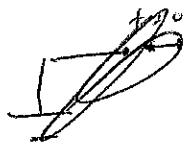
$$V_L = \frac{V_o (1 + R_L)}{1 - (-1) R_L} = V_o$$

SO, UNDER THE STEADY-STATE CONDITIONS, THE LOAD IS CHARGED TO THE BATTERY VOLTAGE. NOTE THAT THIS IS A LOSSLESS TRANSMISSION LINE & THERE IS NO VOLTAGE

LOSS IN THE LINE.

(8)

NEXT, LET US CONSIDER A MORE GENERAL CASE $R_g \neq 0$ AT $t=0$.



$$\text{APPLYING KVL, } V_1^+ = V_0 - I_1^+ R_g$$

$$\text{FOR A FORWARD WAVE: } I_1^+ = V_1^+ / Z_0$$

$$V_1^+ (1 + R_g / Z_0) = V_0$$

or

$$V_1^+ = \left(\frac{Z_0}{Z_0 + R_g} \right) V_0$$

$$\Gamma_L = \frac{R_L - Z_0}{R_L + Z_0} \quad \Gamma_g = \frac{R_g - Z_0}{R_g + Z_0}$$

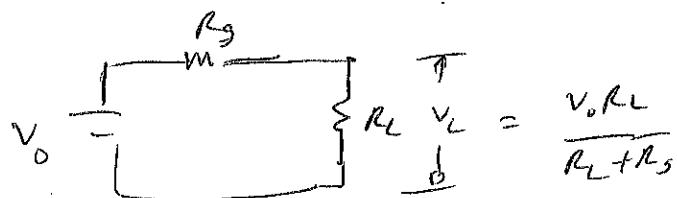
FROM (1),

$$V_L = \left(\frac{Z_0}{Z_0 + R_g} \right) V_0 \times \frac{\left(1 + \frac{R_L - Z_0}{R_L + Z_0} \right)}{1 - \left(\frac{R_L - Z_0}{R_L + Z_0} \right) \left(\frac{R_g - Z_0}{R_g + Z_0} \right)}$$

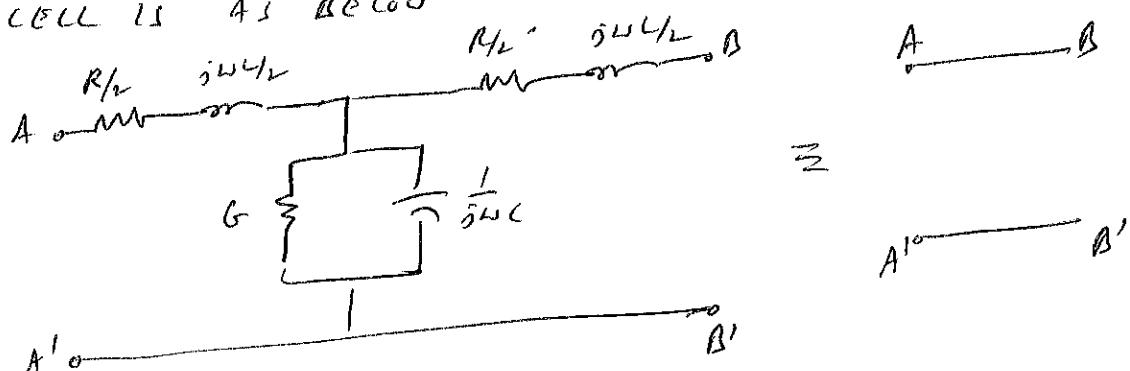
(9)

$$V_L = \frac{\left(\frac{Z_0}{Z_0 + R_S} \right) V_o \times \left(\frac{2R_L}{R_L + Z_0} \right)}{\frac{(Z_0 + Z_0)(R_S + Z_0) - (R_L - Z_0)(R_S - Z_0)}{(R_L + Z_0)(R_S + Z_0)}}$$

$$= \left(\frac{Z_0}{Z_0 + R_S} \right) V_o \times \frac{2R_L \times (R_S + Z_0)}{2Z_0(R_L + R_S)} = \frac{V_o R_L}{R_L + R_S}$$



UNDER THE STEADY STATE CONDITIONS, VOLTAGE ACROSS THE LOAD CAN BE FOUND FROM THE VOLTAGE DIVISION RULES, AS IF THE LINE IS A SHORT CIRCUIT. THIS IS BECAUSE, THE EQUIVALENT CIRCUIT OF THE TRANSMISSION LINE UNIT CELL IS AS BELOW



FOR A LOSSLESS LINE, $R = G = 0$. REACTANCE OVER TO

INDUCTANCE IS $2\pi f L$ ($\because \omega = 2\pi f = 0$ for DC).

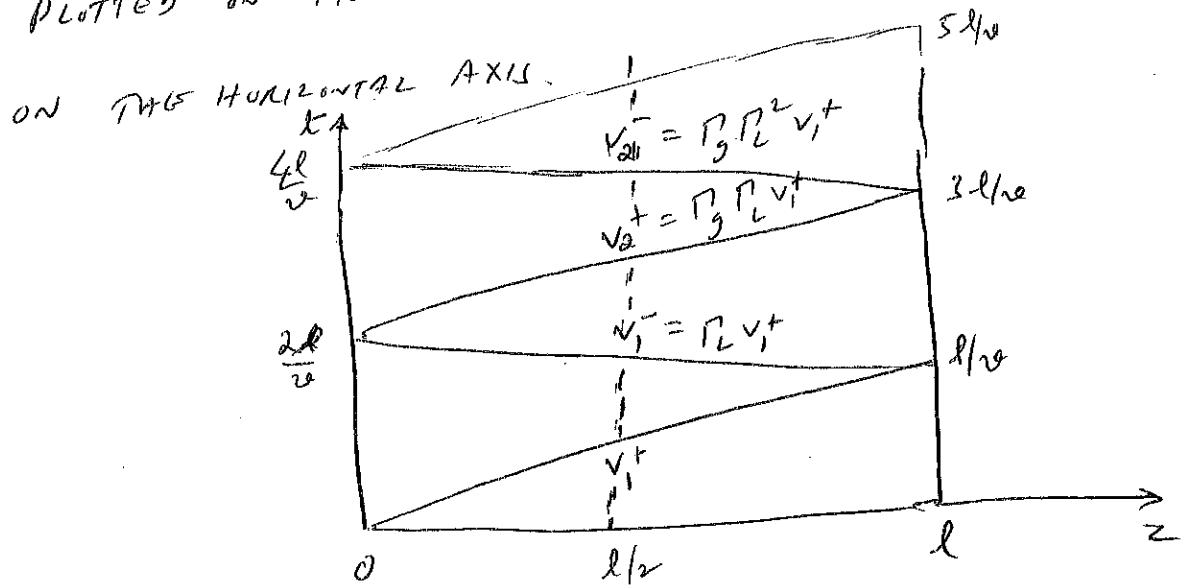
(10)

THE CONDUCTANCE $G = 0 \Rightarrow$ SHUNT RESISTANCE $= \frac{1}{G} = \infty$.
 \Rightarrow OPEN CIRCUIT.

$\omega = 0 \Rightarrow \frac{1}{j\omega C} = \infty \Rightarrow$ OPEN CIRCUIT

(i.e. CAPACITOR BLOCKS THE DC VOLTAGE)

FIG. 6 SHOWS THE VOLTAGE REFLECTION DIAGRAM. TIME IS PLOTTED ON THE VERTICAL AXIS & DISTANCE z IS PLOTTED



At $t=0$, INITIAL VOLTAGE WAVE v_1^+ STARTS AT THE ORIGIN ($t=2=0$). IT REACHES THE LOAD ($z=l$) AT $t=l/v$. THE SLOPE OF THE FIRST DIAGONAL LINE IS $1/v$. v_1^+ IS REFLECTED AT THE LOAD TO PRODUCE A NEW BACKWARD WAVE v_1^- WITH THE AMPLITUDE $v_1^- = P_L v_1^+$, WHICH REACHES THE SOURCE AT $t=2l/v$. v_1^- WAVE IS REFLECTED AT THE SOURCE

(11)

TO PRODUCE A NEW ~~REFLECTED~~ FORWARD WAVE WITH THE

AMPLITUDE $v_2^+ = \rho_s v_1^-$, WHICH REACHES THE LOAD

AT $t = 3l/v$ & SO ON.

THE VOLTAGE AT ANY POINT ON THE LINE AT

A TIME t_1 CAN BE FOUND BY ADDING THE VOLTAGES

AT THAT POINT FROM $t=0$ TO t_1 . FOR EXAMPLE,

TO DETERMINE THE VOLTAGE AT THE MIDDLE OF THE

LINE $(z = l/2)$ AT $t = 4l/v$, ADD THE VOLTAGES

AT $z = l/2$ (VERTICAL DASHED LINE) FROM $t=0$ TO $4l/v$, I.E.

$$V(l/2, 4l/v) = v_1^+ + v_1^- + v_2^+ + v_2^-.$$

EXAMPLE 1

12

Suppose $R_L = R_g = \frac{1}{3} Z_0$

$$P_L = P_g = \frac{R_L - Z_0}{R_L + Z_0} = \frac{\left(\frac{1}{3} - 1\right) Z_0}{\left(\frac{1}{3} + 1\right) Z_0} = -\frac{1}{2}$$

THE STEADY-STATE LOAD VOLTAGE IS

$$V_L = \frac{V_o \times R_L}{R_L + R_g} = \frac{V_o}{2}$$

$$V_i^+ = \frac{V_o \times Z_0}{R_L + Z_0} = \frac{3}{4} V_o$$

$$V_i^- = P_L V_i^+ = -\frac{3}{8} V_o$$

$$V_i^+ + V_i^- = \frac{3}{8} V_o$$

$$V_2^+ = P_g V_i^- = +\frac{3}{16} V_o$$

$$V_i^+ + V_i^- + V_2^+ = \left(\frac{3}{8} + \frac{3}{16}\right) V_o = \frac{9}{16} V_o$$

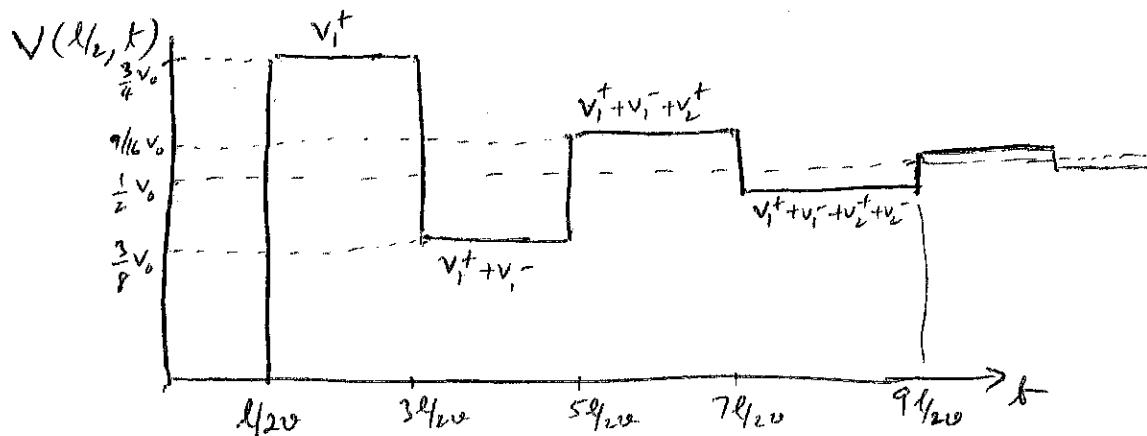


Fig. 2

(12) (13)

FIG. 7 SHOWS THE LINE VOLTAGE AT $z = l/2$. NOTE THAT
 THE LINE VOLTAGE AT $z = l/2$ IS ZERO FOR $t < l/(2v)$
 SINCE IT TAKES $\frac{l}{2v}$ SECONDS FOR THE VOLTAGE WAVE
 TO ARRIVE AT $l/2$ FROM THE BATTERY. THE REFLECTED
 WAVE IS GENERATED AT THE LOAD AT $t = l/v$ & IT
 TAKES $\frac{l}{2v}$ SECONDS TO ARRIVE AT $z = l/2$ SO, V_1^+ IS
 ADDED TO V_1^+ AT $t = \frac{3l}{2v} + 50$ ms.

LINE CURRENT CAN BE FOUND ~~IN~~ IN A SIMILAR WAY THROUGH
 A CURRENT REFLECTION DIAGRAM. THE FORWARD CURRENT

$$\text{is } I^+ = \frac{V^+}{Z_0} \rightarrow (*)$$

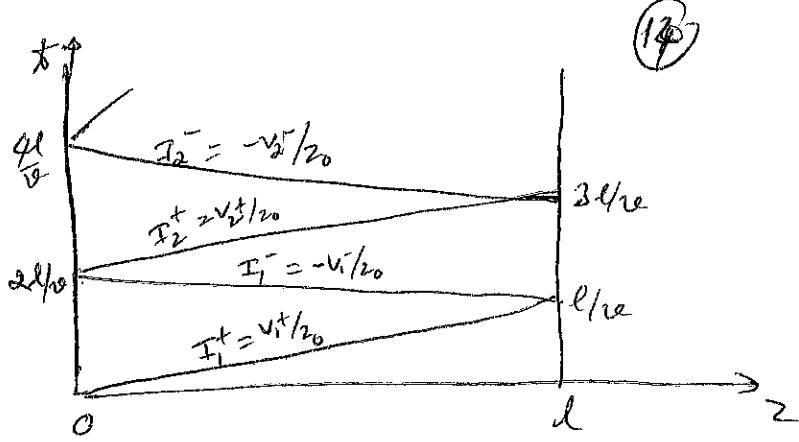
& THE BACKWARD CURRENT IS

$$I^- = -\frac{V^-}{Z_0} \rightarrow (**)$$

DUE TO OPPOSITE DIRECTION

EQU. (*) & (**) ARE VALID FOR ANY ORDER OF REFLECTIONS, i.e.,

$$I_j^+ = \frac{V_j^+}{Z_0} \quad \& \quad I_j^- = -\frac{V_j^-}{Z_0} \quad \text{FOR } j=1, 2, \dots$$



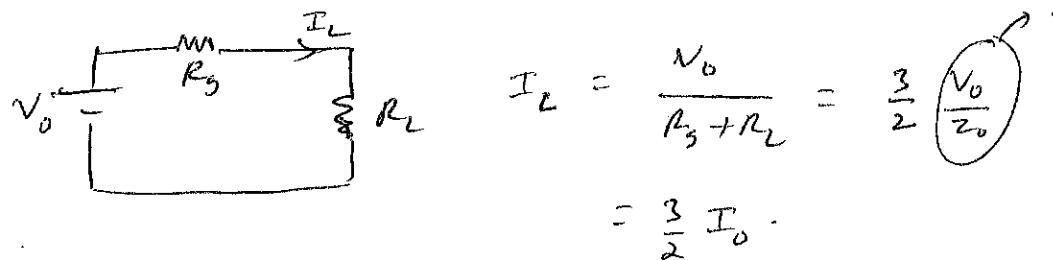
P1h.8

IN EXAMPLE 1₁ (SEE p. 12), $R_L = R_S = 1/3 Z_0$.

THE STEADY STATE LINE LOAD CURRENT IS

CALL IT

I_0



$$\left. \begin{aligned} I_1^+ &= v_1^+/z_0 = \frac{3}{4} \frac{V_0}{Z_0} = \frac{3}{4} I_0 \\ I_1^- &= -v_1^-/z_0 = 3/8 I_0 \\ I_2^+ &= v_2^+/z_0 = \frac{3}{16} I_0 \end{aligned} \right\} \quad \left. \begin{aligned} I_1^+ + I_1^- &= \frac{9}{8} I_0 \\ I_1^+ + I_1^- + I_2^+ &= \frac{21}{16} I_0 \end{aligned} \right.$$

