

Homework from Chapter 3

3.31

$$V_T = (kT/q) = 26.1 \text{ mV}$$

Using the diode V-I relation: $V_D = V_T \ln \left(1 + \frac{I_D}{I_0} \right) = 0.757 \text{ V}$

$$\Delta V = (-1.8 \text{ mV}/k)(50 - 30) = -36 \text{ mV}$$

$$\Rightarrow V_D = 0.757 - 0.036 = 0.721 \text{ V}$$

3.70

A) a. Diode is forward biased

$$\Rightarrow V = -5 + 0 = -5 \text{ V}, I = \frac{5 - (-5)}{100\text{k}} = 100 \mu\text{A}$$

b. Diode is reverse biased

$$\Rightarrow I = 0, V = +7 \text{ V} \text{ (no voltage drop across the resistor)}$$

c. Diode is forward biased

$$\Rightarrow V = 3 \text{ V}, I = \frac{3 - (-7)}{100\text{k}} = 100 \mu\text{A}$$

d. Diode is reverse biased

$$\Rightarrow I = 0, V = +5 \text{ V}$$

B) a. Forward biased $\Rightarrow V = -5 + 0.6 = -4.4 \text{ V}, I = \frac{5 - (-4.4)}{100\text{k}} = 94 \mu\text{A}$

b. Reverse biased $\Rightarrow I = 0, V = +7 \text{ V}$

c. Forward biased $\Rightarrow V = 3 - 0.6 = +2.4 \text{ V}, I = \frac{2.4 - (-7)}{100\text{k}} = 94 \mu\text{A}$

d. Reverse biased $\Rightarrow I = 0, V = -5 \text{ V}$

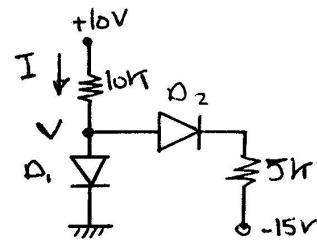
3.71

A) For such kind of circuits, when the diode on/off status isn't clear, we need to put an assumption, then verify its validity. I'll apply this for the first circuit, then solve the others directly using same methodology.

a. Assume D_1 is on & D_2 is off

In this case $V=0$

but this will mean that D_2 is forward biased & should be on, so we've a wrong assumption.



Now assume both D_1 & D_2 are on. $\Rightarrow V=0$

$$\therefore I = \frac{10V}{10\Omega} = 1mA \quad , \quad I_{D_2} = \frac{0 - (-15)}{5\Omega} = 3mA$$

For Kirchhoff's current law to be satisfied $I_{D_1} = I - I_{D_2} = -2mA$

but the current can't flow from N to P, so the assumption is also wrong.

Now, let's assume D_1 is off & D_2 is on $\Rightarrow I = I_{D_2} \Rightarrow I_{D_1} = 0$

$$\therefore I = \frac{10 - (-15)}{10\Omega + 5\Omega} = 1.67mA \quad , \quad V = 10 - 10\Omega(1.67mA) = -6.67V$$

So the diodes operating points will be:

$$D_1 : (0A, -6.67V) \quad D_2 (1.67mA, 0V)$$

b. D_1 & D_2 are both on. $\Rightarrow V=0$

$$I = \frac{15}{5\Omega} = 3mA \quad , \quad I_{D_2} = \frac{0+10}{10\Omega} = 1mA \quad , \quad I_{D_1} = I - I_{D_2} = 2mA$$

$$D_1 (2mA, 0V) \quad D_2 (1mA, 0V)$$

c. Both D_1 & D_2 are on $\Rightarrow V=0$, $I = \frac{10-0}{10k} = 1mA$

$$I_{D_2} = \frac{0 - (-15)}{5k} = 3mA \Rightarrow I_{D_1} = 2mA$$

$D_1 (2mA, 0V)$ $D_2 (3mA, 0V)$

d. D_1 is on & D_2 is off $\Rightarrow V=0$, $I = 1mA$

$$I_{D_2} = 0 \Rightarrow I_{D_1} = I = 1mA$$

$D_1 (1mA, 0V)$ $D_2 (0mA, -15V)$

B) Same way of solution is applied, but taking care that the on diode has a voltage drop of 0.75V

a. D_1 is off & D_2 is on $\Rightarrow I = \frac{10 - (0.75) - (-15)}{10k + 5k} = 1.62mA$.

$$V = 10 - 10k (1.62mA) = -6.2V$$

$D_1 (0mA, -6.2V)$ $D_2 (1.62mA, 0.75V)$

b. D_1 & D_2 are on $\Rightarrow V=0.75V$, $I = \frac{15-0.75}{5k} = 2.85mA$

$$I_{D_2} = \cancel{\frac{0.75 - 0.75 - (-10)}{10k}} = 1mA, I_{D_1} = 1.85mA$$

$D_1 (1.85mA, 0.75V)$ $D_2 (1mA, 0.75V)$

c. D_1 & D_2 are on $\Rightarrow V=-0.75V$, $I = \frac{10 - (-0.75)}{10k} = 1.075mA$

$$I_{D_2} = \frac{-0.75 - 0.75 - (-15)}{5k} = 2.7mA, I_{D_1} = I_{D_2} - I = 1.625mA$$

$D_1 (1.625mA, 0.75V)$ $D_2 (2.7mA, 0.75V)$

$$d. D_1 \text{ is on} \& D_2 \text{ is off} \Rightarrow V = 0.75V, I = \frac{10 - 0.75}{10k} = 0.925 \text{ mA} = I_D,$$

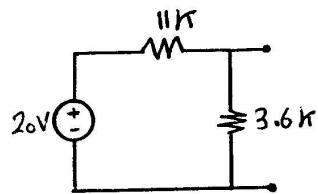
$D_1 (0.925 \text{ mA}, 0.75V) \quad D_2 (0 \text{ mA}, -15.75V)$

3.48

To solve this problem, we need to find the Thevenin eq. of the circuit first.

$$V_{th} = 20V \cdot \frac{3.6k}{11k + 3.6k} = 4.93V$$

$$R_{th} = 11k \parallel 3.6k = 2.71 \text{ k}\Omega$$



$\therefore V_{th} > V_Z$, then the diode is working in the breakdown region

$$I_Z = \frac{4.93 - 4}{2.71k} = 0.343 \text{ mA}$$

$D_Z (0.343 \text{ mA}, 4V)$

3.127

Before solving such problems, we need to consider the following:

1. An ideal capacitor with no connected resistance charges immediately.

2. When no current flows through the capacitor, any voltage rise or drop at one of its plates appears immediately on the other plate.

3. We need to follow the problem at each point in time in order to be able to solve the problem correctly.

I'll apply those rules on the following circuits.

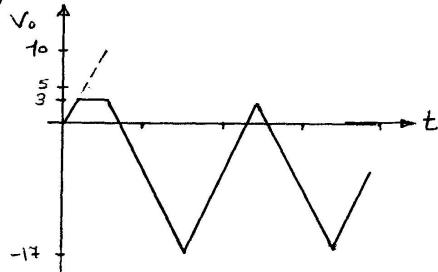
- a. $V_s = 0 \rightarrow 3V$ \therefore The diode is off & no current flows in the capacitor
 \therefore The same voltage is transferred to V_o

$V_s = 3 \rightarrow 10V$ The diode is on & so the capacitor charges from $0 \rightarrow 7V$
 $\therefore V_s = 3V$

$$\begin{array}{l} \cancel{V_s = 10 \rightarrow 3V} \text{ same as above } \Rightarrow V_s = 3V \\ V_s = 10 \rightarrow 3V \therefore V_{cap} = 7V \Rightarrow V_o = (10-7) \rightarrow (3-7)V = 3 \rightarrow -4V \end{array}$$

$V_s = 3 \rightarrow -10V$ The diode remains off & so the o/p voltage follows the i/p voltage but shifted down by $V_c = 7V$

So finally, the o/p voltage will be as follows:



- b. $V_s = 0 \rightarrow 2V$ The diode is on, the capacitor starts charging to 2V
 $\Rightarrow V_o = 2V$
 $V_s = 2 \rightarrow 10V$ The diode is off &
 $V_o = V_s + 2V$
 $V_s = 10 \rightarrow 2V$ The diode is still off &
 $V_o = V_s + 2V$
 $V_s = -2 \rightarrow -10V$

$V_s = 0V$ The diode is on & it charges to $-2V$ & $V_o = 2V$

$V_s = 0 \rightarrow 10V$ The diode is off & $V_o = V_s + 2V$

$V_s = 10 \rightarrow 0V$ The diode is off & $V_o = V_s + 2V$

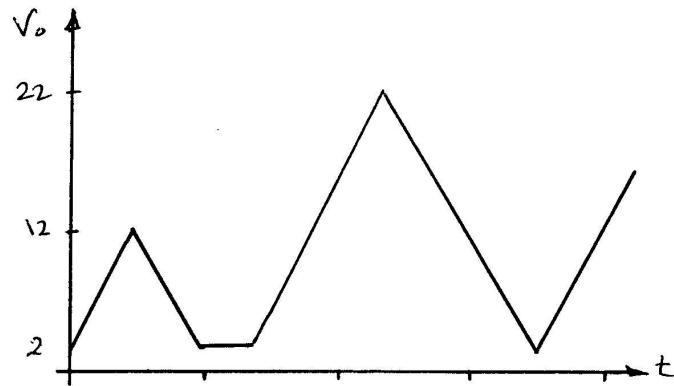
$V_s = 0 \rightarrow -10V$ The diode is on & so the capacitor charges to $-12V$
 $\& V_o = 2V$

$V_s = -10V \rightarrow 0V$ The diode is off & $V_o = V_s - (-12) = V_s + 12V$

$V_s = 0 \rightarrow 10V$ The diode is off & $V_o = V_s + 12V$

$V_s = 10 \rightarrow 0V$ The diode is still off & $V_o = V_s + 12V$

$V_s = 0 \rightarrow -10V$ The diode remains off & $V_o = V_s + 12V$



3.129

For both diodes to be conducting $\rightarrow V_o = V_2 + V_{on} = 5.7V$

$V_s = 0 \rightarrow 5.7V$ Both diodes are off & so $V_o = V_s$

$V_s = 5.7 \rightarrow 10V$ Diodes are conducting & the cap charges to $4.3V$ & $V_o = 5.7V$

$V_s = 10 \rightarrow 0 \rightarrow -10V$ Diodes are off again & $V_o = V_s - 4.3V$

$V_s = -10 \rightarrow 0 \rightarrow 10V$ Diodes remain off & $V_o = V_s - 4.3V$

Diodes remain off forever & $V_o = V_s - 4.3V$

