

Temperature coefficient = $-1.8 \text{ mV}/^\circ\text{C}$

$$\Delta t = 5^\circ$$

$$\therefore \Delta V = -1.8 \times \Delta t = -9 \text{ mV}$$

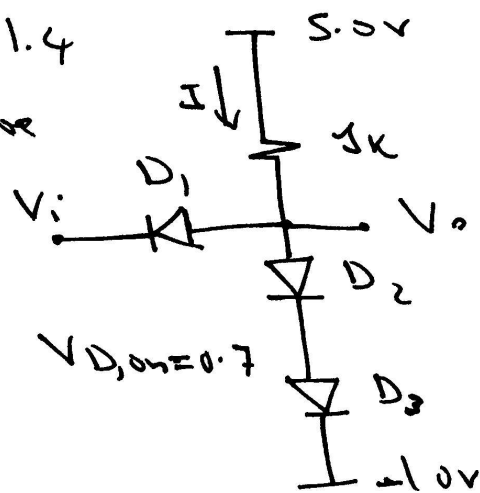
$$\therefore V_n = V + \Delta V = 0.68 - 0.009 = 0.671 \text{ V}$$

if D_1 is on $\Rightarrow V_o = 1.4$

$\Rightarrow D_2$ and D_3 must be on

if D_2 and D_3 are on $\Rightarrow V_o = 0.4 \text{ V}$

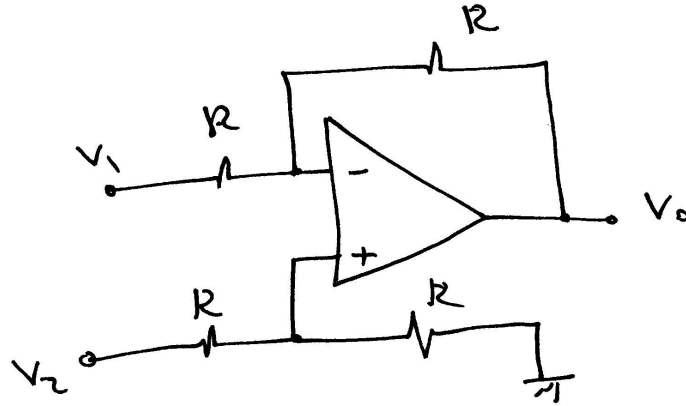
$\Rightarrow D_1$ is off



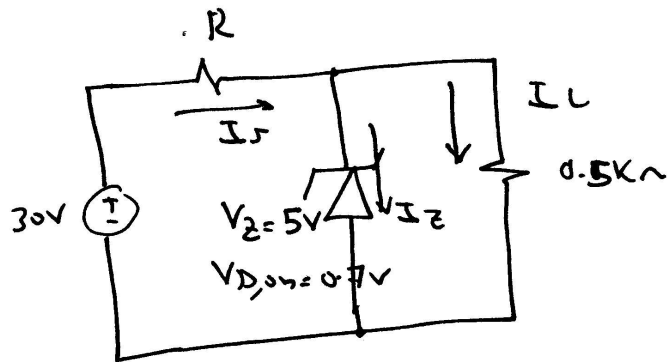
only possible state is that

$$D_2 \text{ and } D_3 \text{ are on } \Rightarrow I = \frac{5.0 - 0.4}{1k}$$

$$I = 4.6 \text{ mA}$$



$$V_0 = (V_2 - V_1) \implies A_{cm} = 0$$



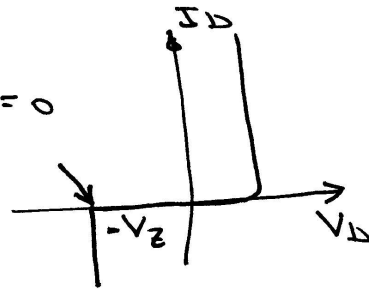
$$I_s = I_L + I_z$$

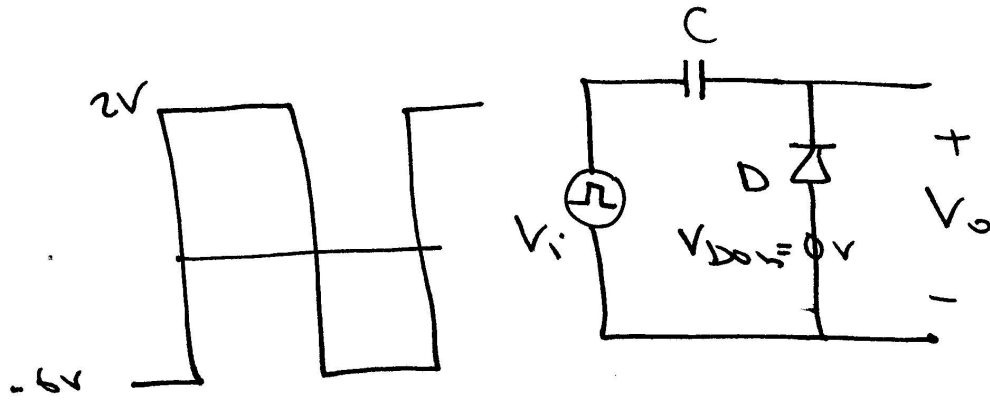
Largest value makes $I_z = 0$

$$I_s = I_L$$

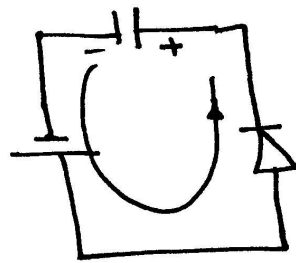
$$\frac{30 - 5}{R} = \frac{5}{0.5K}$$

$$R = 2.5K\Omega$$



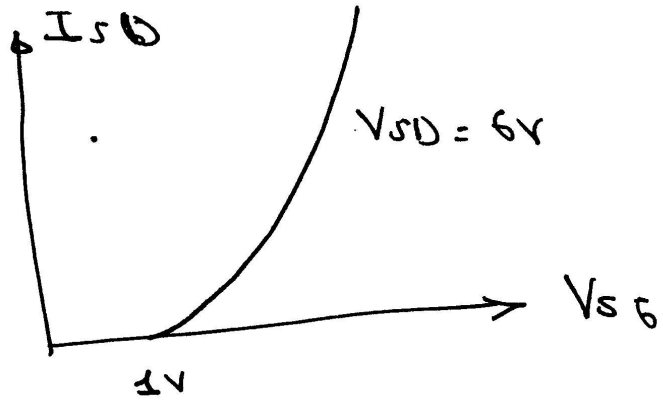


During -ve input part, diode is on,
and Capacitor charges to 6V



During +ve input part, diode is off,

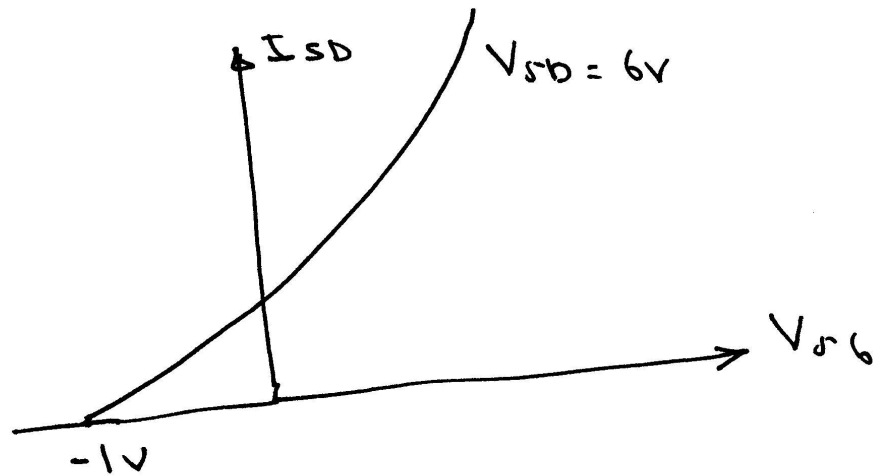
$$V_o = V_i + 6V \Rightarrow 0 \leq V_o \leq 8V$$



On Condition

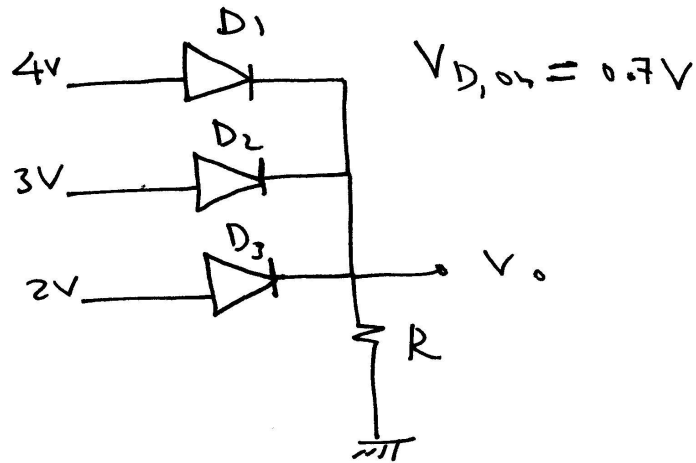
$$V_{GS} > -V_{TP} \Rightarrow V_{TP} = -1V$$

enhancement type PMOS



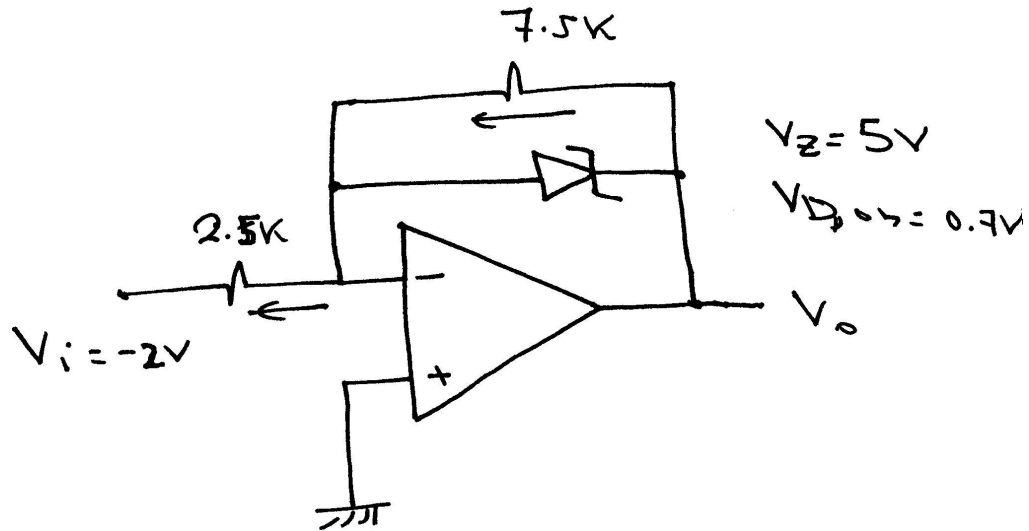
$$V_{GS} > -V_{TP} \Rightarrow V_{TP} = 1V$$

depletion type PMOS



8 possible states

- * if D_2 or D_3 are on $\implies D_1$ is on
 - * if D_1 is on $\implies D_2$ and D_3 are off
 - * Diodes can not be off simultaneously
 - * only one possible state
 - D_1 is on, D_2 and D_3 are off
 - $V_o = 3.3V$
-



* Zener diode Can not be forward biased because of polarity of current

* Assume that diode is in cut-off region (open-circuit) $\Rightarrow V_o = 6\text{V} > V_Z$

It follows that Zener is in breakdown region $\Rightarrow V_o = 5\text{V}$